

		200
Instruction	Туре	<u>Operation</u>
Α	Producer Load	LOAD (address1, r1)
В	Calculation	ADD (r1, r2, r3)
С	Consumer Load	LOAD (address2=r3, r4)
D	Independent Load	LOAD (address3=r5, r6)

FIG. 2

DETERMINE CACHE "MISS" STATE CONDITION (OP 416) LOGICAL ANDing (OP 418) SQUASH MEMORY ACCESS IP ANDing IS "TRUE"	
COMMENCE "B"" PICK "C" (OP 414))
302F) !
PICK "B" (0P 412)	
30.7 F. C.) 1
COMMENCE COPYING ADDRESS 1 DATA TO CACHE; GENERATE TAG "T1" & COMPARE WITH TAG IN RAM FOR CACHE "MISS' OPTERMINATION (OP 410)	,
	j >
GENERATE ADDRESS (OP 408)	
30°C) 1
COMMENCE A (OP 406)	
302B) 1
UPDATE DYNAMIC MEMORY DEPENDENCY TABLE (OP 403) PICK "A" (OP 404)	
302A	i))

FIG. .

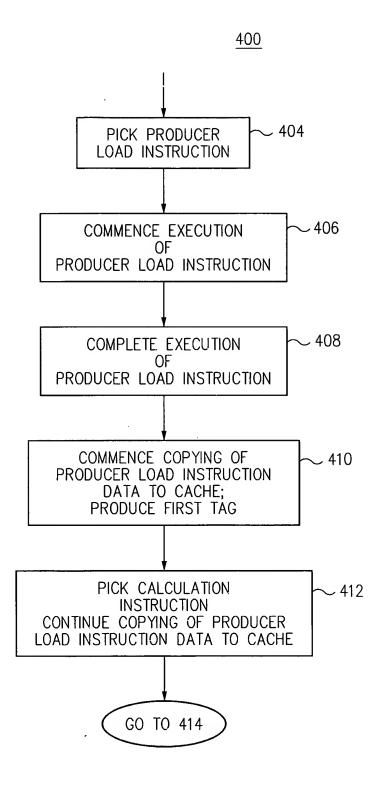


FIG. 4A

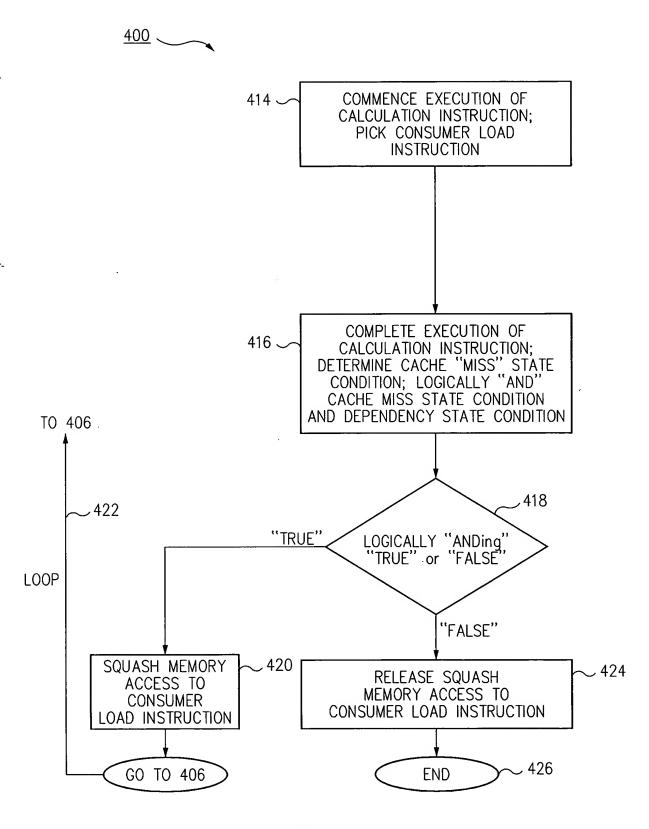


FIG. 4B

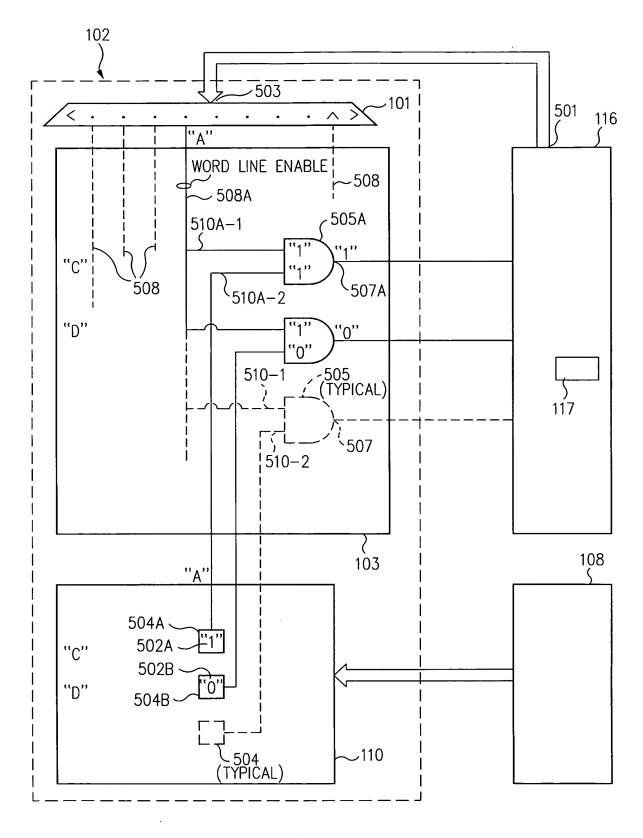


FIG. 5